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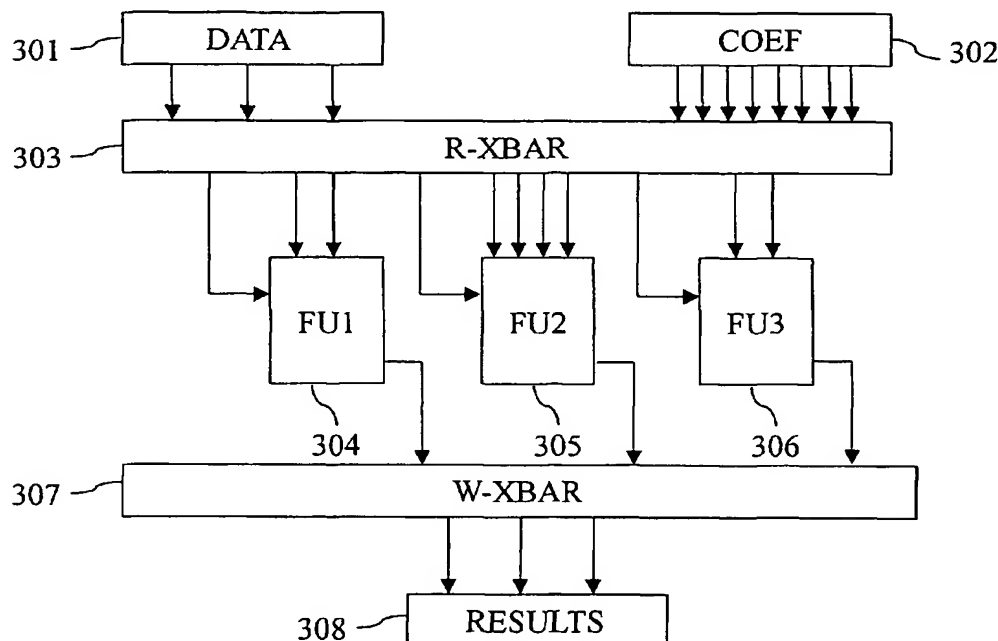
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(54) Title: DATA PROCESSING CIRCUIT



(57) Abstract: The invention relates to a data processing system for performing a polyphase filtering. This circuit comprises functional units (304, 305, 306) able to perform polyphase filterings, and a memory device (301, 302, 308) for storing data and coefficients. The functional units receive in parallel data and coefficients coming from the memory device, calculate results from the data and coefficients and supply these results to the memory device.



— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

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B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 942 530 A (KONINKL PHILIPS ELECTRONICS NV) 15 September 1999 (1999-09-15) paragraph '0014! - paragraph '0018!; figure 2	1-8
A	US 5 383 145 A (SAKIYAMA SHIRO ET AL) 17 January 1995 (1995-01-17) cited in the application column 4, line 3 - column 6, line 63; figures	1-8
A	US 2001/007573 A1 (KINGSTON SAMUEL C ET AL) 12 July 2001 (2001-07-12) paragraph '0059! - paragraph '0069!; figures 1B, 3A, 7	1-8

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INTERNATIONAL SEARCH REPORT

International Application No
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>US 4 785 411 A (GERGEN JOSEPH P ET AL) 15 November 1988 (1988-11-15) column 3, line 39 - column 4, line 55 column 6, line 65 - column 7, line 37 figures 2,6</p> <p>-----</p>	1,3-8

INTERNATIONAL SEARCH REPORT

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International Application No

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